



PATENT
Attorney Docket No. 10018224-1
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)	Blaine D. Gaither, et al.	Examiner	Henry Tsai
Serial No.	10/002,971	Group Art No.	2181
Filed	October 26, 2001	Confirmation No.	3480
For	Method Of Optimization Of CPU And Chipset Performance By Support Of Optional Reads By CPU And Chipset		

February 13, 2006

Mail Stop AF
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE

Dear Sir:

Responsive to the Final Office Action mailed December 13, 2005, please
amend the above-titled application (hereinafter the '971 Application) as follows:

IN THE CLAIMS begins on page 2 of this paper.

REMARKS begin on page 5 of this paper.